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PATENT
Attorney Docket No. 402767

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MORIWAKI et al.

Art Unit: 2811

Application No. 10/650,661

Examiner:

Filed: August 29, 2003

For: HIGH-FREQUENCY INTEGRATED
CIRCUIT DEVICE HAVING HIGH
EFFICIENCY AT THE TIME OF LOW
POWER OUTPUT

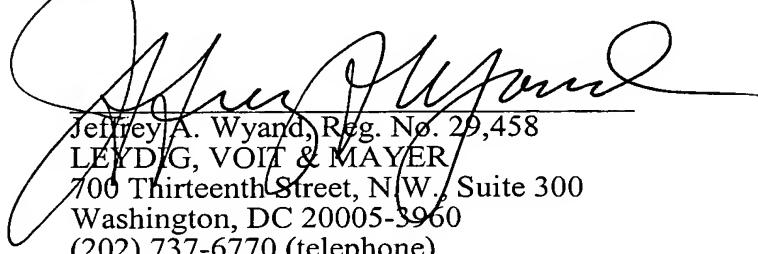
REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

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Arlington, VA 22202

Dear Sir:

An error has been noted on the Official Filing Receipt in the referenced application, namely, the assignee's name is misspelled. Please issue a Corrected Official Filing Receipt correcting this error as marked in red on the attached copy of the Official Filing Receipt.

Respectfully submitted,


Jeffrey A. Wyand, Reg. No. 29,458
LEYDIG, VOIT & MAYER
100 Thirteenth Street, N.W., Suite 300
Washington, DC 20005-3960
(202) 737-6770 (telephone)
(202) 737-6776 (facsimile)

Date: *February 4, 2005*



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/650,661	08/29/2003	2811	750	402767	2	4	1

CONFIRMATION NO. 7786

23548
 LEYDIG VOIT & MAYER, LTD
 700 THIRTEENTH ST. NW
 SUITE 300
 WASHINGTON, DC 20005-3960

FILING RECEIPT



OC000000011329999

11/24/2003

Date Mailed: 11/21/2003

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Applicant(s)

Takao Moriwaki, Tokyo, JAPAN;
 Hiroyuki Otsuka, Hyogo, JAPAN;

Assignment For Published Patent Application

Mitsubishi Denki Kabushiki Kaisha, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant DENKI

Foreign Applications

JAPAN 2003-63070 03/10/2003

If Required, Foreign Filing License Granted: 11/20/2003

Projected Publication Date: 09/16/2004

Non-Publication Request: No

Early Publication Request: No

Title

High-frequency integrated circuit device having high efficiency at the time of low power output

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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